Computer Simulation of the Reliability of Wire Bonds and Ribbon Bonds in Power Electronics Modules

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Abstract

Aluminium wires are widely used in power electronics modules to connect power semiconductor devices and other parts of the module electrically. Recently, other interconnect techniques have been proposed such as ribbon bond to improve the reliability, performance and reduce costs of power modules. The reliability of ribbon bond technique for an IGBT power module under power cycling is compared with that of conventional wire bond in this study using electro-thermal nonlinear Finite Element Analysis. The results showed that a single ribbon of 2000µm x 200µm will replace three wire bonds of 400µm in diameter to achieve a similar module temperature distribution under same power load. Using the equivalent plastic strain increment per cycle, it is seen that the ribbon bond is more reliable than the wire bonds. The impact of neglecting joule heat in the wire/ribbon bonds during power cycling simulation has also been investigated.

1 Introduction

Power electronics systems using semiconductors such as metal oxide semiconductors field effect transistors (MOSFET), insulated gate bipolar transistors (IGBT) and diodes are increasingly being used in many applications ranging from electrical traction applications in elevators, electric vehicles, subways and locomotives, power generation and transmission, computing, health care, aviation and aerospace, all requiring high power density and high reliability.

To meet the increasing demand for high power density power electronic systems with higher current carrying capability and switching frequency while at same time moving towards miniaturisation, low-cost and reduced weight trends as well as reducing some inherent failure mechanism of power electronic components, power electronics designers and manufacturers are devising new packaging methods and materials to meet such demands. This has led to development of efficient interconnect designs to meet demands such as low on state resistance [1] and high temperature operating materials. One of such measures is through changing the wire geometry by the introduction of Aluminium (Al) ribbon bonds for power chips interconnections - an alternative from the conventional round Al wire bonds. The advantages of the ribbon over round wire bonds are mainly the larger surface area in relation to the cross sectional area providing lower parasitic and lower impedance at high frequency. This geometric feature gives the ribbon a lower electrical resistivity than a wire of the same length and therefore a higher current handling capability as can be seen in (1) and (2). This allows less use of multiple bond wires to meet a current requirement or fulfill a necessary resistance.

$$R_w = \frac{\rho_o L}{A_w} = \frac{\rho L}{\pi r^2} \tag{1}$$

$$R_r = \frac{\rho_o L}{A_r} = \frac{\rho_o L}{wt}$$
(2)

where R_w and R_r are wire and ribbon resistance respectively, ρ_o is the material resistivity, L is the length or span of the wire/ribbon, t is the ribbon thickness, r is the wire radius A_r and A_w are the cross-sectional area of the wire and ribbon respectively.

Furthermore, the issue of cratering defects during wire bonding is less with Al ribbon than with round wire especially during initial stage of bonding process because the ribbon's larger surface area allows better distribution of bond force and ultrasonic energy [2]. Another benefit of the Al ribbon over wire bonding is that the reduced number of bonds means less defects during bonding process and therefore higher quality and throughput. The larger surface area of the ribbon also means that it has a better heat conduction capability and can remove joule heating generated around the chip – pad interface faster.

Due to the rectangular shape of the ribbon, a better continuous contact bonding area can easily be implemented on a bond pad die metallization thereby eliminating the non-continuous contact that is often the case when multiple wires terminate on the same bond pad which results in high current entering the semiconductor which may lead to local differences in heat dissipation as well as the electric field strength [3]. Furthermore, compared to the same surface area, less bonding force and ultrasonic power is required to deform and bond the ribbon because of its lower cross-sectional area/thickness compared to round wire.

Miniaturization of the power electronic systems is made easier by using ribbon bonding whilst increasing the power density and the frequency of power electronics applications, this is due to the fact that increasing diameter of wires for high power capability creates larger loops and a bigger casing. However, due to the planar nature of the ribbon, reduced thickness and lower loop height are possible for high power density application.

Ribbon bonds are used extensively for high frequency applications such as optical and optoelectronic devices and in the past recent years, have been developed for power electronic applications such as in compact DC- DC MOSFET converters like D^2 PAK and quad-flat-no-lead (QFN) packages [4] but to the author's knowledge has not been used for a commercial IGBT power module.

1.1 Wire bond failure modes

Figure 1 shows the two major failure modes: bond wire lift-off at bond foot and heel crack [5]. The bond wire lift-off happens more frequently.

The wire bond failure is mostly due to thermo-mechanical fatigue from temperature swings that are resulted from power dissipation in the power semiconductors during switching and conduction, and from heat generation in the wires due to ohmic heating. During the operation of a power electronic system, high electric current flow from one region of a power module device to another, especially from the emitter terminal to the diode anode connection. The currents cause temperature increase that result in stress concentration at the interface between the bond wire and the bond pad which may lead to horizontal cracks and failures at the bond interface [6]. When the wires are heated repeatedly, the flexure of the wire may lead to vertical crack and failure at the heel [7]. The thermo-mechanical stresses are intensified due to the CTE mismatch between the Al bond wire and the silicon chip that it is bonded.

The effect of wire bond failure results in changes in either contact resistance value or internal distribution of current leading to increased temperature above the safe operating area (SOA).

The thermo-mechanical behaviour of gold ribbon used in optoelectronics application has been investigated by Tan et al [8] using pull and shear tests under different temperature ranges and aging time. This study showed that ribbon bonds have higher pull and shear strength than round wire bonds. Celnikier et al [5] performed reliability tests for D^2 PAK package using passive thermal cycling for ribbon bond and compared with wire bonds. In [9], the mechanical properties of Al-ribbon bonds such as tensile strength at different temperatures were investigated and using pressure cooker tests for high temperature storage and aging tests for reliability were performed. The high temperature storage tests showed low intermetallic compound (IMC) of Al_2 Cu formation as result of low diffusion and concluded that the risk of ribbon bond failure on direct copper bonding (DBC) caused by diffusion is very low, while the pull tests after thermal storage tests decreases with storage time caused by recrystallization softening of Al and copper Cu.

The limitation of these studies is that the reliability of the ribbon bonds based on heat generation from Joule heating and cyclic power loss in the power device especially for IGBT

power modules has not been investigated and compared with wire bonds, therefore, its real failure modes and mechanism has not been characterized. Hence, the need to model its thermo-mechanical behaviour and compare with the conventional round wire bonds for IGBT modules under power cycling as well investigate the impact of neglecting the wire joule heating during power cycling simulation (as this is often the case in the literature) on the computed number of cycles to failure.



Figure 1: Failure modes in wire bonds

2 Methodology

Two IGBT power modules models are shown in the Figures 2 and 3 with wire bond and ribbon bonds respectively. They are analysed in this study using the Finite Element Method (FEM). The IGBT power module dimensions, layout and characteristics are similar to that of Powerex IGBT half-bridge module CM150DU-24NFH– a commercial power IGBT module [10].



Figure 2: IGBT module with wire bonds



Figure 3: IGBT module with ribbon bonds



The IGBT power module is made of several layers of different materials as shown in the Figure 4.

Figure 4: Schematic of IGBT power module

2.1 Finite element analysis (FEA) simulation

The power cycling simulation was performed using FEA and a commercial multi-physics FEM software ANSYS has been used. Power cycling is realized by switching heat generation in the module on and off using the second-level [11] method since the study is focused on the wire/ribbon bonding with a constant turn on (heating) and turn off (cooling) times of 4 seconds and 11 seconds respectively giving a 15 seconds cycle period.

In the simulation, a power loss of 207 W and 175 W are applied to the IGBT power chips and diodes respectively which corresponds to 100 amperes of current density in the wires or ribbons.

At the bottom of the baseplate, the convective heat transfer coefficient value is $2000 \text{ W/m}^2\text{C}^\circ$ during cooling and $5 \text{ W/m}^2\text{C}^\circ$ during heating. Two loading conditions are applied. In the first one heat generation is applied in the chip only and in the second one it is also applied to the wires/ribbons giving 4 cases. Case 1 and 2 is the ribbon bond

and wire bond without joule heating while case 3 and 4 is the ribbon bond and wire bond with joule heating respectively.

All the materials are modelled as linear elastic with exceptions of solder and the aluminium wire/ribbon. The solder material is 96.5Sn3.5Ag and it has been modelled as a viscoplastic material using the Anand's model. The aluminium wire/ribbon bonds have been modelled as elastic-plastic material with isotropic hardening. The material properties used in this study is presented in Table 1 and obtained from [12] and the Anand's parameters [12] is given in Table 2.

Considering the size of the models, a sub-modelling technique was adopted whereby, the full model was modelled with coarse mesh and then a section of interest (wire and ribbon bonds) was taken and refined with a very fine mesh.

2.2 Thermo-Mechanical analysis

In FEM, thermo-mechanical analysis can be performed as direct (fully) coupled phenomenon by solving simultaneously the strain-heat transfer (3) and temperature/displacement (4) equation using a coupled element while considering material nonlinearities.

$$\nabla . (k\nabla T) - \rho C \frac{dT}{dt} - (3\lambda + 2\mu)\dot{\varepsilon}_{ii}\alpha T_0 = -\dot{q}$$
(3)

$$\mu u_{i,kk} + (\lambda + \mu)u_{k,ki} - (3\lambda + 2\mu)\alpha\Delta T = 0$$
(4)

where k is the material thermal conductivity, ρ is the density, C is the specific heat capacity, \dot{q} is the heat generation rate, u is the displacement, ε is the strain, λ and μ are the Lame's constants and ΔT is the temperature change from a zero strain reference temperature.

In this study, sequentially coupled thermal-stress analysis has been used. The temperature distributions in the power module as a result of the heat dissipation and joule heating is computed for a transient thermal analysis using (5) while (6) is used to compute the joule heat in the wire/ribbon.

$$\nabla . (k \nabla T) + \dot{q} = \rho C (dT)/dt$$
(5)

$$Q = I^2 R = I^2 \rho_o L/A \tag{6}$$

where: Q is the wire/ribbon joule heat.

Then, the temperature distribution is used as a thermal load to compute the thermal force vector (7) which is used to calculate the transient stress and strain distribution in the power module.

$$F_{th} = E\alpha\Delta T \tag{7}$$

where E is the young's module and α is the CTE. When discretised over the domain in FEM formulation, (7) becomes

$$F_{th} = \int B^T D\{\alpha \Delta T\} dv \tag{8}$$

where B^T is the transpose of the derivatives of element shape functions, D is linear isotropic fourth order Hooke's tensor.

Table 1: Material properties.

Material properties	Silicon	Al	Cu	Aluminium	Solder
				Nitride	
Young's modulus (GPa)	113	70	103.42	310	54.05-0.19T
Poisson's ratio	0.29	0.33	0.3	0.24	0.4
CTE (1/K)	2.6	23.1	17	5.6	21.85+0.02039T
$k(W.W^{-1}.K^{-1})$	150	235	400	160	54
Electrical resistivity (Ω, m)	0.0001	2.65 x10 ⁻⁸	1.7 x10 ⁻⁸	1 x10 ¹⁴	1.3 x10 ⁻⁷
Density (kg/m^3)	2330	2700	8930	3300	8400
Specific heat (J/kgK)	729	950	400	740	150
Yield stress (MPa)	-	31 [11]	-	-	-
Tangent modulus (MPa)	-	45.7 [11]	-	-	-

Table 2: Material parameters for the Anand viscoplastic model.

Parameters	S _o (MPa)	Q/R (K)	$A(s^{-1})$	ير ب	m_0	h_0 (MPa)	ŝ (MPa)	n	a
Solder	39.09	11262	2.23 x10 ⁴	6	0.182	3321.15	73.81	0.0018	1.82

2.3 Life Prediction model

The life prediction model used for predicting the number of cycles to failure N_f is given in (9) and is based on the equivalent plastic strain increment per cycle $\Delta \bar{\varepsilon}^p$ [9].

$$N_f = B_1 \Delta \bar{\varepsilon}^{p B_2} \tag{9}$$

where B_1 and B_2 are constants given in Table 3.

Table 3: Constants for life prediction model [9].

B ₁	<i>B</i> ₂
113.189	-1.223

3 Results & Discussion

The temperature history for power devices (juncture) for case 1 and 2 under power cycling is shown in Figure 5 while a typical temperature distribution in the power module at the end of the heating time in shown in Figure 6. The Figure 3 shows that the temperature history is similar for the two power module designs. The average temperature history in the power chips for the four cases is shown in Figure 8 while Figure 7 shows the average temperature histories for the wire and ribbon bonds. It is seen, that average chip temperature is similar in all cases with about 1 to 2 °C increase in temperature for case 3 and 4.



Figure 5: IGBT juncture temperature history for case1 and 2

However, the introduction of the joule heat obviously increases the average ribbon and wire bond temperatures as is seen in Figure 7.



Figure 6: IGBT power module temperature distribution for case 1.



Figure 7: Average wire and ribbon temperatures



Figure 8: Average power chip temperature for all cases

The Figures 9 and 10 show the equivalent plastic strain distribution in the ribbon and wire bond respectively at the end of the power cycling.



Figure 9: Equivalent plastic strain distribution in the ribbon



Figure 10: Equivalent plastic strain distribution in the wire

The Figure 9 showed that equivalent plastic strain distribution is concentric in the ribbon bond and maximum at the corners where crack initiation is expected to occur and propagate inwards leading to possibly bond lift being the dominant failure as a result of CTE mismatch. The equivalent plastic strain distribution in the wire bond is maximum at the heel and also distributed at the bond interface showing a heel crack dominant failure mode due to wire flexure as well as a bond wire lift off failure mode. The Figure 11 shows that equivalent plastic strain is always higher in the wire bond than in the ribbon for both loading cases and then using the life prediction model and equivalent plastic strain increment per cycle, the ribbon bond has more number of cycles to fail than the wire bond as shown in the Table 4.

Table 4: Computed number of cycles to failure for each case.

	Case 1	Cas2	Case3	Cas4
N _f	7922	7339	6381	5529



Figure 11: Equivalent plastic strain history in the wire and ribbon bonds for all cases

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4 Conclusion

The results have shown that if a single ribbon of 2000 μ m by 200 μ m is used to replace three 400 μ m diameter wires, the temperature in the ribbon and wires will be almost identical. The stress and strain distribution in the ribbon bond shows that bond lifting is the dominant failure mechanism whereas heel cracking is also a significant failure mechanism in conventional wire bonds because of wire flexing. This shows that the ribbon bonding could not only be more economical for large current capability but also more reliable than wire bonding. This research has also found that in the simulation, it is important to consider the joule heating in the wire in addition to the heat dissipation in the chips during power cycling simulation.

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